Preliminary Product Brief
November 2013

Proc High Speed (PHS) Daughterboards
TM
For ProceV and future boards

Overview
GiDEL line of ProceV Stratix V based boards provides a powerful FPGA system with massive connectivity and tremendous throughput performance of up to 8000 GB/s for internal memory and over 25 GB/s for on board memory. The ProceV’s unique architecture enables exceptional connectivity flexibility including CXP, SFP+, RJ45 and High-Speed inter-board connectors. For additional connectivity and unique protocol interfaces, GiDEL offers two type of Daughter boards: PSDB Daughterboard family including Camera Link, HDMI, SDI, etc., and Proc High Speed (PHS) Daughterboard family designed to interface of Altera Stratix V and future generation high-speed transceivers operating at throughput rates greater then 12.5 Gb/s.

PHS Daughterboard Family

1. **PHS_cXp6x4**: CoaXPress Interface
   - 4 × Frame Grabber receive lines
   - 4 × Device transmit lines used for ProcCamSim™ Simulator

2. **PHS_3QSFP**: 3×QSFP Interface

3. **PHS_SAS**: SAS or SATA Interface
   - 2 × Mini-SAS HD
   - 8 × SATA
   - Up to 12 × SATA per ProceV board

4. **Customized PHS**: open mechanical and connectivity design-set enabling user customized PHS daughterboards.

For information on additional PHS daughterboards, contact GiDEL.