

For Immediate Release

Gidel Launches New High Performance Line of Acceleration Boards Based on Intel's Stratix 10 FPGA

Proc10S FPGA board purpose designed for high-density Big Data and HPC applications.

Santa Clara, California, and Or-Akiva, Israel, November 8, 2017 – Gidel, a technology leader in high-performance accelerators utilizing FPGAs, today launched their latest product line, the Proc10S. The Proc10S is part of the Proc family of high performance, scalable compute acceleration boards, but is based on the Stratix 10 FPGA, which was released by Intel in late 2016. The Stratix 10 represents twice the performance gain over the Arria 10, with 30% lower power consumption per TFLOP.

The Proc10S pushes data processing power to new heights with peak single precision performance of up to 10 TFLOPS per device, based on 25 MB of L1 cache at up to 94 TB/s peak bandwidth. The board features an Intel Stratix 10SG 2800/2100/1100 FPGA with 16-lane PCI-Express Gen 3.0 and an 18+ GB multi-level memory structure consisting of three banks of DDR4 memory on board and on DIMMs (up to 260 GB of DDR4).

With up to 2.8 million logic elements, the Proc10S gives designers incredible performance potential. It also features flexible high-speed communication ports — dual SFP+ and dual QSFP+ support at 26 Gb/s per channel — and a PHS connector for a high speed daughter board that features eight channels of full duplex Tx/Rx and up to 139 Gb/s total.

Gidel's newest acceleration board was designed with high density Big Data and HPC applications in mind. "The Proc10S is a heavy-duty FPGA and thus opens new markets in HPC for Gidel, such as Deep Learning and Big Data analytics," says Ofer Pravda, VP Marketing and Sales at Gidel. "Gidel's long history in algorithm acceleration utilizing FPGA technology has resulted in an enormous wealth of product knowledge that provides us with an advantage in certain HPC and Vision arenas."

Artificial Intelligence and Deep Learning are ideal markets for the Proc10S because features need to be extracted from data in order to solve predictive problems, such as image classification and detection, image recognition and tagging, network intrusion detection, and fraud/face detection. Other applications include compute intense algorithm processing, network analytics, communications, cyber security, storage, big data analytics, and cloud computing.

The Proc10S is supported by the ProcDeveloper's Kit™, Gidel's proprietary tools that make developing on FPGA fast and easy, and allow for simultaneous acceleration of multiple applications or processes, unmatched HDL design productivity (VHDL or Verilog), and simple integration with software applica-

tions. Gidel's tools make developing on FPGA accessible to software engineers by automatically generating an Application Support Package (ASP) and an API that maps the relevant user's variables directly into the FPGA design. The tools offer a solution that is unique in the market, and together with Intel's HLS and OpenCL allow unmatched development efficiency and effectiveness.

The Proc 10S 2100/2800 will be available in Q1 2018; additional Proc10S accelerators will be released later next year.

Visit Gidel in booth 1242 at SC17 in Denver, Colorado (Nov 13-16) to explore the Proc10S board and view demos on acceleration applications.

About Gidel

For 25 years, Gidel has been a technology leader in high performance, innovative, FPGA-based accelerators. Gidel's reconfigurable platforms and development tools have been used for optimal application tailoring and for reducing the time and cost of project development. Gidel's dedicated support and its products' performance, ease-of-use, and long-life cycles have been well appreciated by satisfied customers in diverse markets who continuously use Gidel's products, generation after generation. For more information visit www.gidel.com.

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